

10

10

15a
SENSOR
MODULE

15b
SENSOR
MODULE

14
INTERNET

11
CENTRAL
COMPUTER

12
DISPLAY
MONITOR

13
RECEIVER

15e
SENSOR
MODULE

15c
SENSOR
MODULE

15d
SENSOR
MODULE

Figure 1

FIG. 2 is a block diagram of a system 20 for detecting a target object. The system 20 includes a processor 21, a register 26, an IR sensor 22, a transmitter 23, and a battery 25. The IR sensor 22 is connected to the processor 21. The processor 21 is connected to the transmitter 23. The transmitter 23 is connected to an antenna 24. The register 26 is connected to the processor 21. The battery 25 is connected to the processor 21 and the transmitter 23.

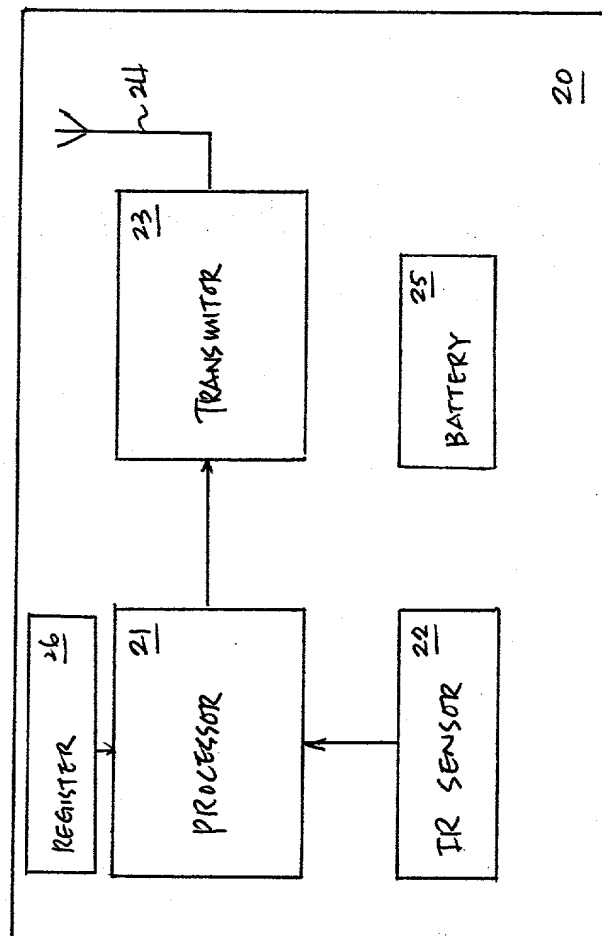


FIGURE 2

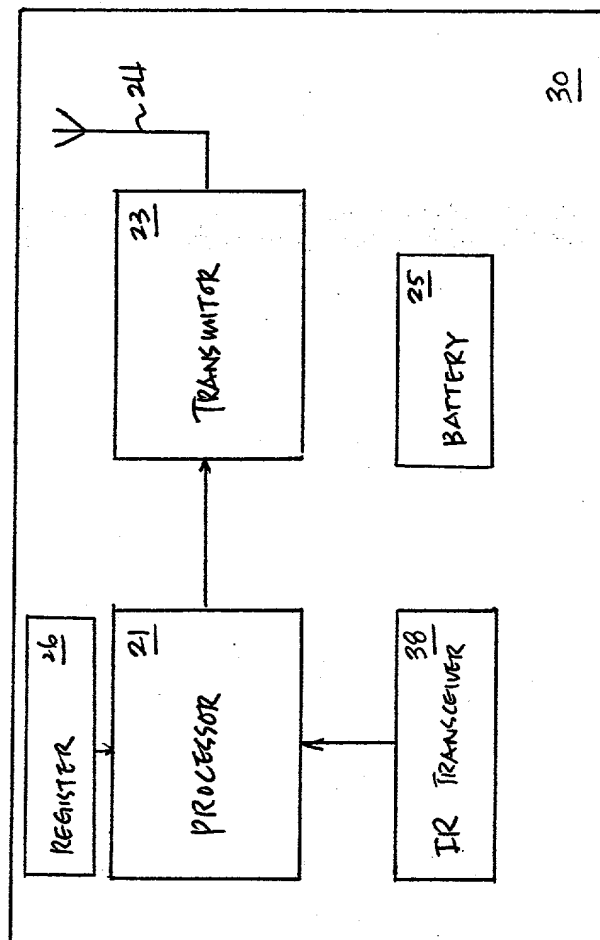


FIGURE 3

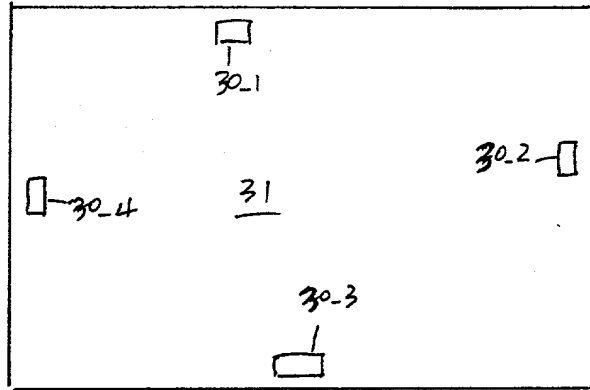


FIGURE 3A

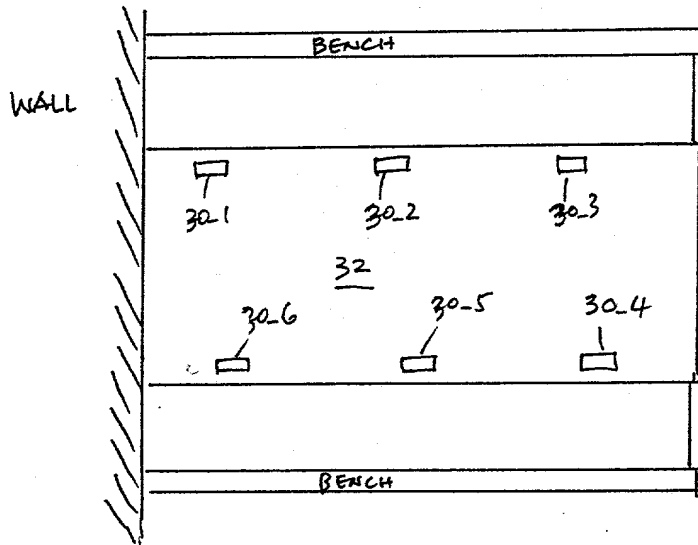


FIGURE 3B

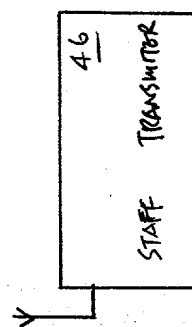
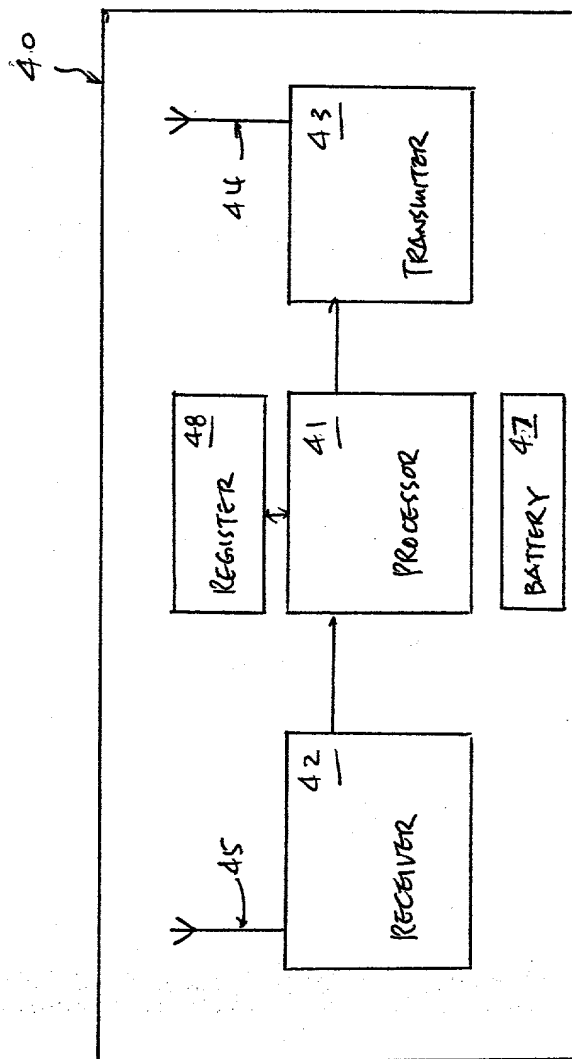


Figure 4

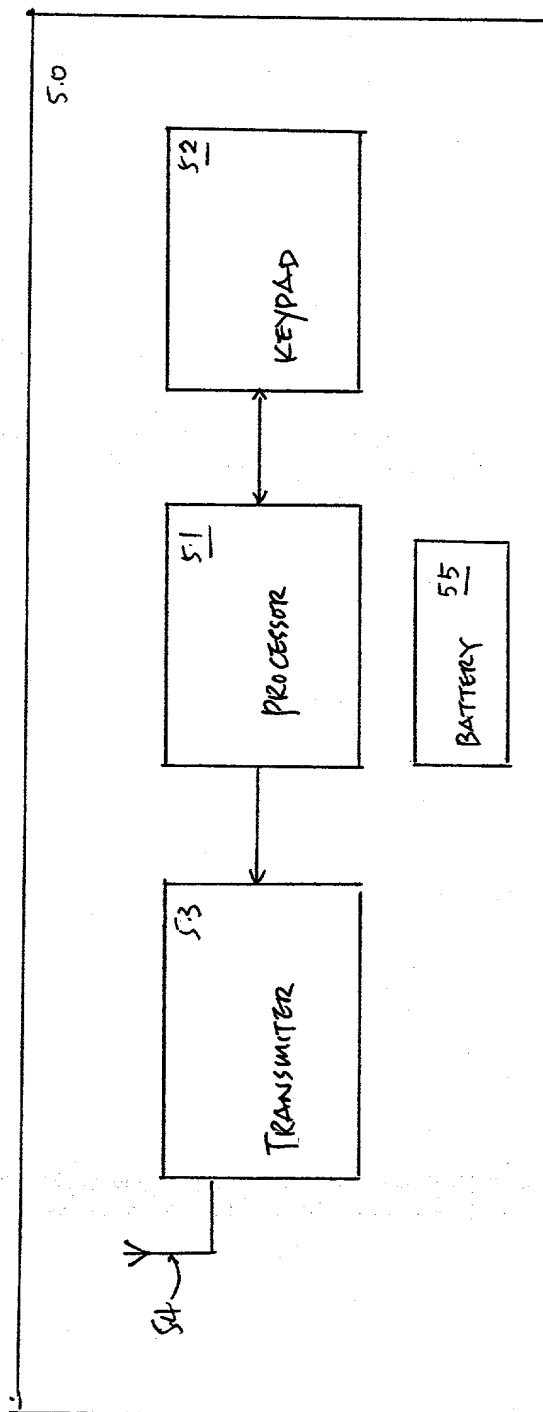


FIGURE 5

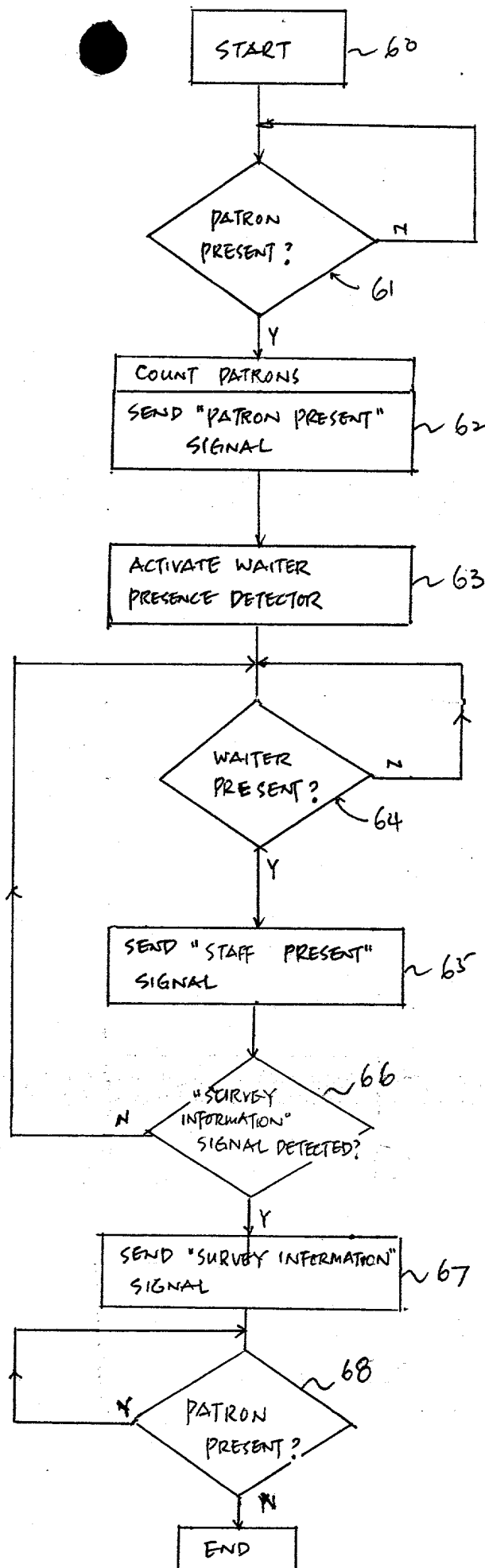


FIGURE 6